

JUN 10 2005

S/N 09/943,134

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Applicant: Leonard Forbes et al. Examiner: Tu-Tu V. Ho  
Serial No.: 09/943,134 Group Art Unit: 2818  
Filed: August 30, 2001 Docket: 1303.020US1  
Title: PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH  
ASYMMETRICAL TUNNEL BARRIERS

AMENDMENT UNDER 37 C.F.R. § 1.312

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Notice of Allowability mailed April 7, 2005,  
please amend as follows:

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COMMUNICATION CONCERNING RELATED APPLICATION(S)

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 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11/062543	February 22, 2005	1303.028US2	SRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
11/063825	February 23, 2005	1303.132US1	GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicant requests that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

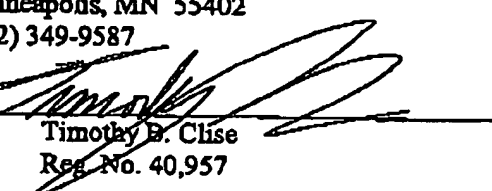
Respectfully submitted,  
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By Applicants' Representatives,  
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Date

10 June '05

By

  
 Timothy B. Clise  
 Reg. No. 40,957

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

Kacia Lee

June 10, 2005  
 Date of Transmission